

ABSTRACT

An emulator couplable to an integrated circuit, the integrated circuit including a micro controller, a DMA controller, memory means and an ADC and the emulator being operable to read a block of data stored in a plurality of memory locations of the memory means and graphically to display a representation of at least a portion of the block of data. An integrated circuit including an ADC and reprogrammable non-volatile memory means, the ADC being arranged to read calibration values from the non-volatile memory means. An integrated circuit including a microcontroller, a DAC selected from a group consisting of a string DAC and a current source DAC and an operational amplifier coupled to the DAC output. An integrated circuit includes analog and digital circuitry, and has only two polysilicon layers on a C-MOS substrate.

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